flow - captor

Type *4114.70 / 4114.03*

Installation and Adjustment Instructions

Please read carefully: No liability can be accepted for damage caused by improper use of the captor.

1.0 Items delivered

1.1 flow-captor 4114.70 / .03
1.2 Union nut, 1¼"
stainless steel WN 1.4305 (303)
1.3 O-ring for 1¼"
1.4 Screwdriver for adjustment

2.0 Installation Instructions:

2.1 Installation depth: min. 5 mm
min. clearance: 5xD upstream, 3xD downstream
(D=pipe diameter)
2.2 Orientation to flow: see drawing
2.3 Installation site: preferably in vertical, rising pipe, or horizontal pipe with flow-captor mounted horizontally.

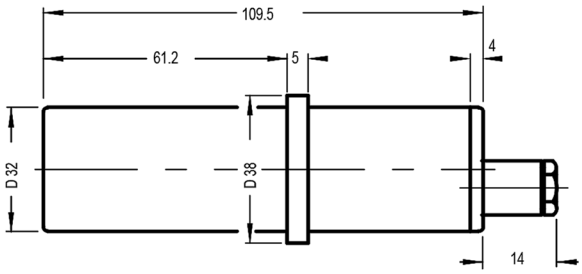
2.4 Installation: Push O-ring over the sensing surface and housing up to the flange. Insert flow-captor into a welded fitting and hold in place with the union nut.
Ideal sealing is achieved by a fitting with a 4-5 mm wall (can be supplied on request).

2.5 Initial operation: Connect the flow-captor to the voltage (see connection diagram and lable) and wait approx. 5 minutes before adjusting.

3.0 Adjustment Procedure:

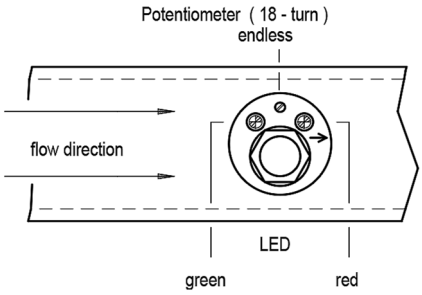
3.1 Bring system flow rate to level at which signal is required.
3.2 Switch-point lower than normal flow-rate:
If necessary, turn the pot. to the left until green LED lights up (flow rate above set-point). Sub sequently, turn pot. slowly to the right until the red LED is on and the green LED is off (set-point now corresponds to flow rate).
3.3 Switch-point higher than normal flow-rate:
If necessary, turn the pot. to the right until the red LED is on (flow below set-point). Subse quently, turn pot. slowly to the left until the red LED is off and green LED is on (set-point now corresponds to flow rate).
3.4 NB. Normal flow rate must be above (see 3.2) or below (see 3.3) the respective set-point as un der flow rate fluctuations the slight hysteresis (< 15%) may lead to different signal conditions.

Dimensions (mm)



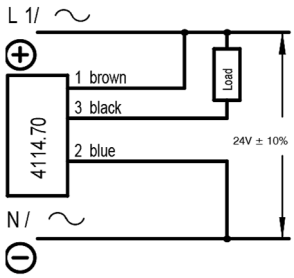
Installation
Union nut 1¼"
Spanner gauge 50 mm

Positioning
Rear view of flow-captor

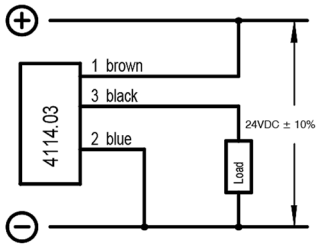


Connection Diagram

AC/DC-transistor output
DC:NPN output



DC:PNP-transistor output



weber
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